

AMENDMENT UNDER 37 C.F.R. § 1.111
U.S. Appl. No. 09/722,306
Attorney Docket Q61090

IN THE CLAIMS:

1. (Currently Amended) A ladder circuit editing system for inputting and editing a sequence program for a program controller in the form of a ladder diagram, ~~said ladder circuit editing system having an unavailable area in which at least one circuit pattern that has been input is stored and an available area in which a new circuit pattern is stored, and successively inputting circuit patterns from said available area, said ladder circuit editing system comprising:~~

an unavailable area for storing a plurality of previously stored circuit patterns of circuit elements;

an available area for storing circuit elements of a circuit pattern being input;

a circuit pattern extracting means for comparing making a comparison between a circuit element contained in a of the circuit pattern being input and stored in said available area with a corresponding circuit element contained in a circuit pattern one of the plurality of previously stored circuit patterns stored in said unavailable area, and extracting from the plurality of previously stored circuit patterns a an extracted circuit pattern in which the comparison indicates agreement between the compared circuit elements that contains a circuit element agreeing with a stored one;

a display means for displaying the extracted circuit pattern extracted by said circuit pattern extracting means on an input screen; and

a copying means for copying the circuit pattern extracted by said circuit pattern extracting means into said available area according to an operator's entry in response to an input by an operator.

2. (Currently Amended) A ladder circuit editing system according to ~~Claim~~ claim 1, wherein;

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when more than one said extracted circuit pattern is extracted, said display means successively displays, for selection by an operator, the a plurality of extracted circuit patterns extracted by said circuit pattern extracting means, and in response to the selection of one of the plurality of extracted circuit patterns, said copying means copies any the selected extracted circuit pattern, ~~which is selected based on the operator's entry from among the plurality of successively displayed circuit patterns~~, into said available area.

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3. (Original) A ladder circuit editing system according to claim 2, wherein said display means displays a previously selected circuit pattern as a top priority.
4. (Currently Amended) A ladder circuit editing system according to claim 3, further comprising:
a selected circuit pattern address storage area ~~in which~~ storing the address of a previously selected circuit pattern is stored, and a
replacing means for placing the address of a selected circuit pattern at the head of said selected circuit pattern address storage area,
wherein said display means displays as a top priority the previously selected circuit pattern according to the order of addresses stored in said selected circuit pattern address storage area.
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IN THE DRAWINGS.

Applicant encloses as an Appendix a replacement drawing sheet which includes Fig. 14.